

FIG. 1

CAT.1: BIT PATTERN PASSED

UNALTERED TO THE

HDD INTERFACE

SIGNAL WIRES

BITS THAT FALL IN TO

CAT.1 & CAT.2: PASSED

ONTO HDD I/F WIRES &

INTERPRET INTERNALLY

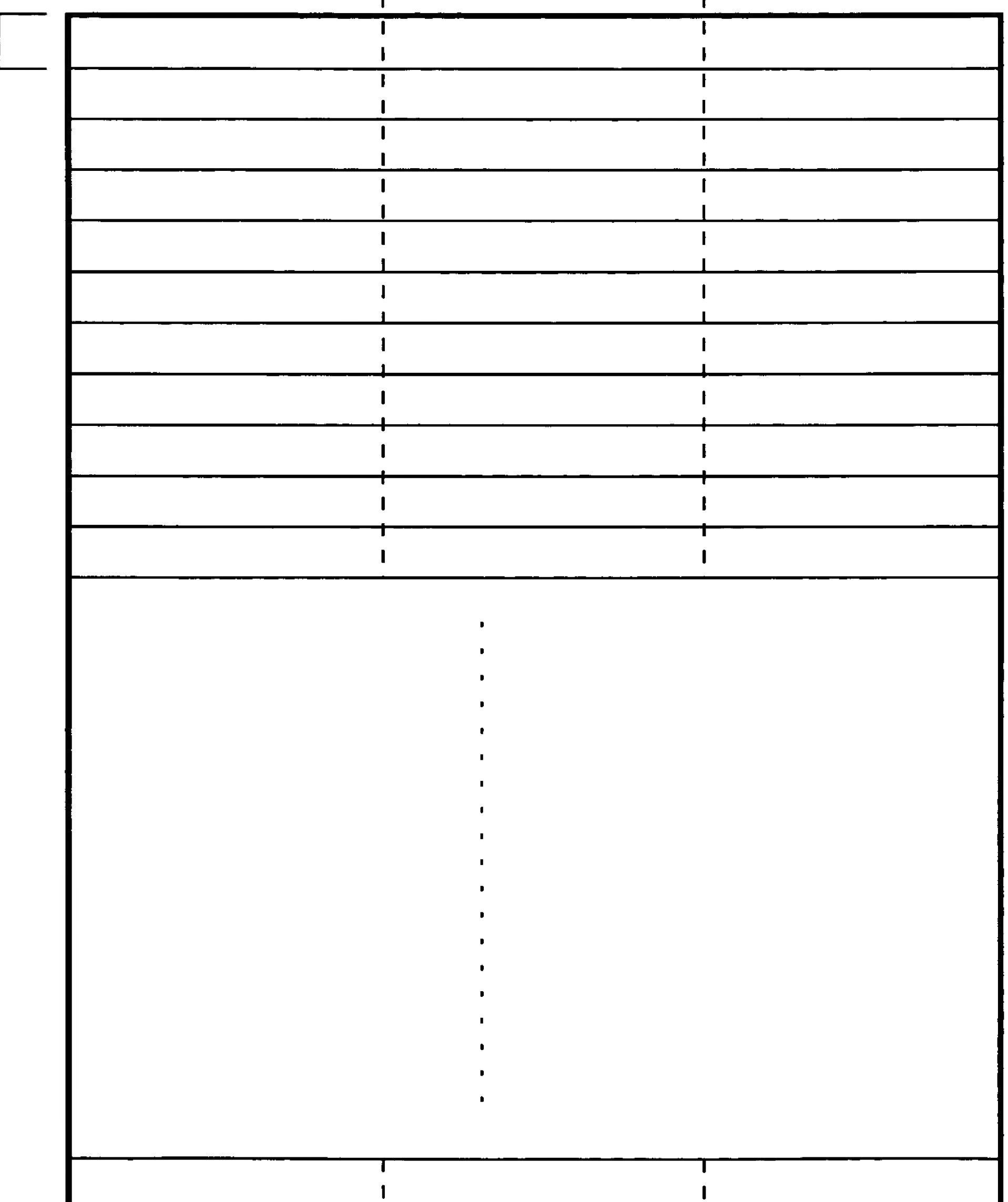
CAT.2: BITS USED TO

DEFINE & INSTIGATE

A BULK DATA TRANSFER

(AND OTHER INTERNAL

OP'S)



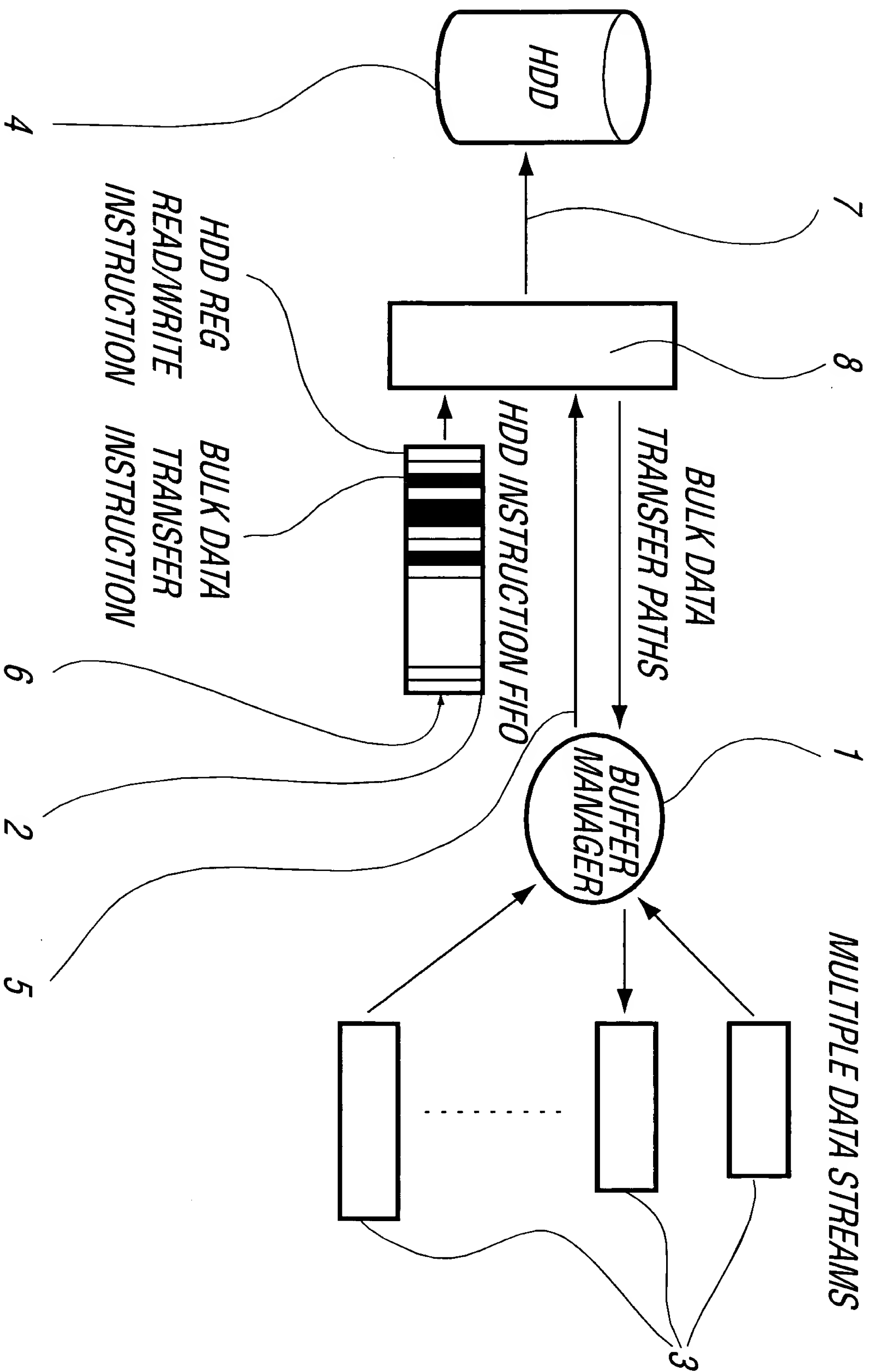
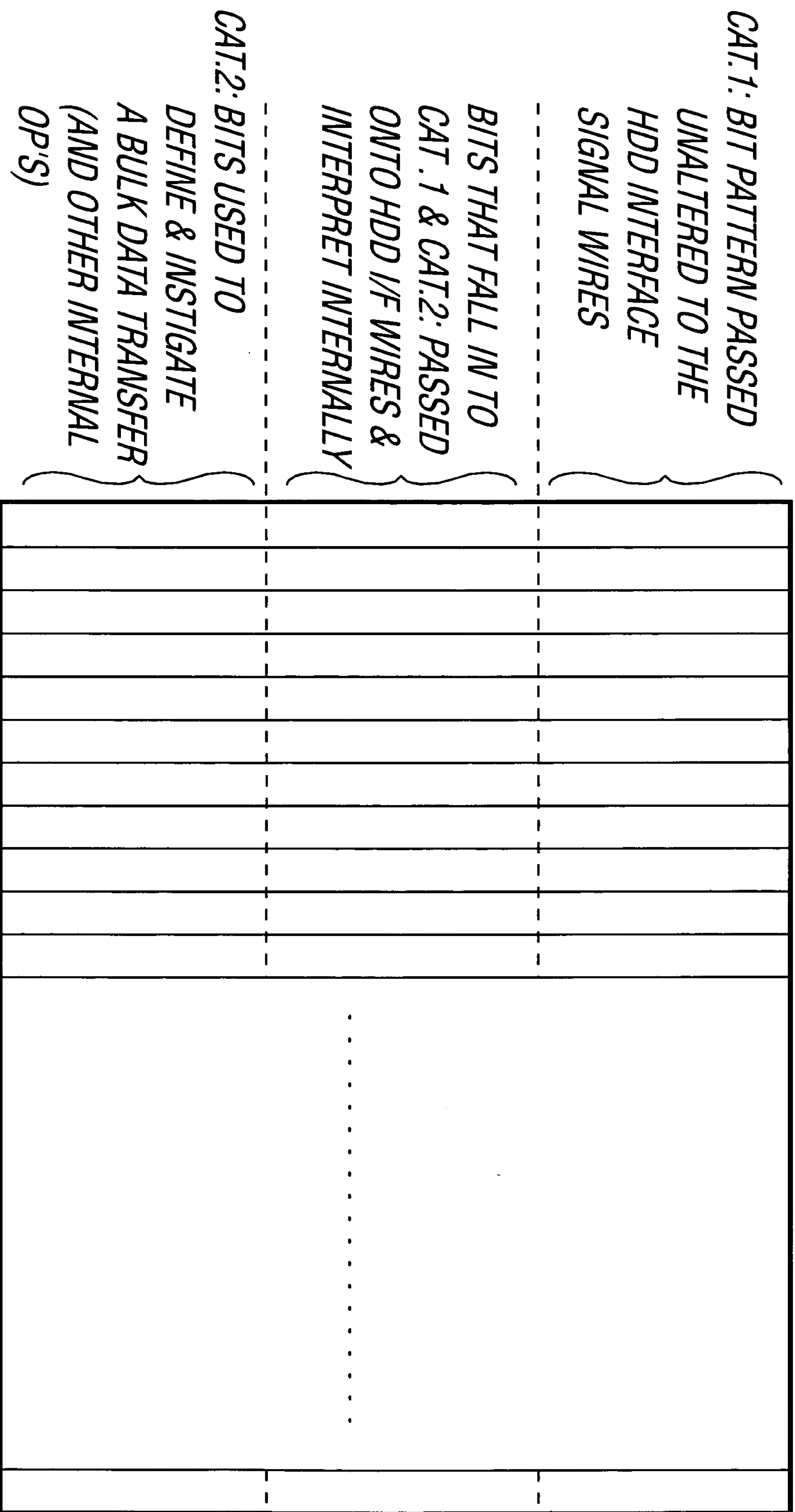


FIG. 1



One HDD Instruction FIFO Entry

FIG. 2